

**Proposed
Draft**

**Serial ATA
International Organization**

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Title: Correction to description of APS status in Identify Packet Device

Proposed correction to Serial ATA Revision 3.0

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Document History

Version	Date	Comments
1	04/07/2010	Initial draft
2	05/17/2010	Minor editorial change

1 Introduction

This ECN corrects an error in the description of Automatic Partial to Slumber status in Identify Packet Device.

2 Summary of the problem

The description of the word 79 in Identify Packet Device from the section 13.2.2 of SATA 3.0 contains the bit 7 as a reserved bit. The bit is for ATAPI devices to indicate the current APS status.

		F	0	Shall be cleared to zero
79	O	R	15-7	Serial ATA features enabled
		V	6	1 = Software settings preservation enabled
		V	5	1 = Asynchronous notification enabled
		R	4	Reserved
		V	3	1 = Device initiating interface power management enabled
		R	2-1	Reserved
		F	0	Shall be cleared to zero
80-87				Set as indicated in ATA8-ACS

This appears to be a typographical error.

3 Proposed correction

The corrected word 79 description in Identify Packet Device should be as following.

79	O	R	15-8	Reserved
		V	7	1 = Device Automatic Partial to Slumber transitions enabled
		V	6	1 = Software settings preservation enabled
		V	5	1 = Asynchronous notification enabled
		R	4	Reserved
		V	3	1 = Device initiating interface power management enabled
		R	2-1	Reserved
		F	0	0 Shall be cleared to zero