

**Proposed
Draft**

**Serial ATA
International Organization**

**Version 0.1
Aug. 24, 2010**

**Serial ATA Revision 3.0 ECN # 048
Title : RX Impedance Balance Correction**

This is an internal working document of the Serial ATA International Organization. As such, this is not a completed standard and has not been approved. The Serial ATA International Organization may modify the contents at any time. This document is made available for review and comment only.

Permission is granted to the Promoters, Contributors and Adopters of the Serial ATA International Organization to reproduce this document for the purposes of evolving the technical content for internal use only without further permission provided this notice is included. All other rights are reserved and may be covered by one or more Non Disclosure Agreements including the Serial ATA International Organization participant agreements. Any commercial or for-profit replication or republication is prohibited. Copyright © 2000-2010 Serial ATA International Organization. All rights reserved.

This Draft Specification is NOT the final version of the Specification and is subject to change without notice. A modified, final version of this Specification ("Final Specification") when approved by the Promoters will be made available for download at this Web Site: <http://www.sata-io.org>.

THIS DRAFT SPECIFICATION IS PROVIDED "AS IS" WITH NO WARRANTIES WHATSOEVER, INCLUDING ANY WARRANTY OF MERCHANTABILITY, NON-INFRINGEMENT, FITNESS FOR ANY PARTICULAR PURPOSE OR ANY WARRANTY OTHERWISE ARISING OUT OF ANY PROPOSAL, SPECIFICATION, OR SAMPLE. Except for the right to download for internal review, no license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted or intended hereunder.

THE PROMOTERS DISCLAIM ALL LIABILITY, INCLUDING LIABILITY FOR INFRINGEMENT OF ANY PROPRIETARY RIGHTS, RELATING TO USE OF INFORMATION IN THIS DRAFT SPECIFICATION. THE PROMOTERS DO NOT WARRANT OR REPRESENT THAT SUCH USE WILL NOT INFRINGE SUCH RIGHTS.

THIS DOCUMENT IS AN INTERMEDIATE DRAFT FOR COMMENT ONLY AND IS SUBJECT TO CHANGE WITHOUT NOTICE.

* Other brands and names are the property of their respective owners.

Copyright © 2005-2010 Serial ATA International Organization. All rights reserved.

1 Introduction

1.1 Problem Statement

The Gen2i RX Impedance Balance included 5.0GHz – 6.5GHz. This frequency range only applies to Gen3i.

1.2 Solution Summary

Remove the number 4 from the 5.0GHz – 6.5GHz row in Gen2i RX Impedance Balance.

1.3 Background (optional)

2 Technical Specification Changes

2.1.1 7.2.1 Physical Layer Requirements Tables

[Editor's Note: The changes marked in red (and underlined/strikethrough) will be incorporated in section 7.2.1

This includes removal of the number 4 and replacing it with a hyphen "-".]

$RL_{DC11,RX}$ RX Impedance Balance (all values Min)	dB	150 MHz- 300 MHz	-	30	30	-	30	7.2.2.3.5	7.4.13
		300 MHz- 600 MHz	-	30	30	-	30		
		600 MHz- 1.2 GHz	-	20	20	-	20		
		1.2 GHz- 2.4 GHz	-	10	10	-	10		
		2.4 GHz- 3.0 GHz	-	4	4	-	10		
		3.0 GHz- 5.0 GHz	-	4	-	-	4		
		5.0 GHz- 6.5 GHz	-	4	-	-	4		