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## Document History

<table>
<thead>
<tr>
<th>Version</th>
<th>Date</th>
<th>Comments</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>May 31, 2012</td>
<td>Initial release.</td>
</tr>
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</table>
1 Introduction

1.1 Problem Statement
In SATA rev. 3.1 there exists two HFPDMAQ11: ErrorFlush state machines with different descriptions and different branches.

1.2 Solution Summary
Replace the second HFPDMAQ11: ErrorFlush state machine with HFPDMAQ13: SendStatus.

1.3 Background (optional)
## 2 Technical Specification Changes

### 2.1 <Title of section being changed>

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## 3 [Editor's note 12] Host Command Layer protocol

### 3.1 [Editor's note 12.1] FPDMA QUEUED command protocol overview

This high-level state machine describes the behavior of the host for the Native Command Queuing command protocol. The host behavior described by the state machine may be provided by host software and/or host hardware and the intent of the state machines is not to indicate any particular implementation.

This class includes:

- a) READ FPDMA QUEUED;
- b) WRITE FPDMA QUEUED;
- c) NCQ QUEUE MANAGEMENT;
- d) RECEIVE FPDMA QUEUED; and
- e) SEND FPDMA QUEUED.

### 3.2 [Editor's note 12.1+1] FPDMA QUEUED command protocol

<table>
<thead>
<tr>
<th>State</th>
<th>Action/Transition</th>
</tr>
</thead>
<tbody>
<tr>
<td>HFPDMAQ11: ErrorFlush</td>
<td>Retire failed queued command with status set to error condition reported by device. Flush all allocated native queued command tags. Flush pending native queued commands from host command queue with system-specific error condition or re-issue pending queued commands.</td>
</tr>
<tr>
<td>1. Unconditional</td>
<td>→ HFPPIO: Idle</td>
</tr>
<tr>
<td>HFPDMAQ12: RetrieveRequest SenseDMA</td>
<td>Receive Data FIS with Queued Error Log contents</td>
</tr>
<tr>
<td>1. Data FIS reception complete</td>
<td>→ HFPDMAQ13: SendStatus</td>
</tr>
<tr>
<td>HFPDMAQ113: SendStatus ErrorFlush</td>
<td>Request transmission of a Register Device to Host FIS</td>
</tr>
<tr>
<td>1. FIS transmitted</td>
<td>→ HFPDMAQ11: ErrorFlush</td>
</tr>
</tbody>
</table>

**HFPDMAQ11: ErrorFlush**: When in this state, the Command layer retires the failed queued command with the error status set to the error condition reported by the device. It flushes all allocated native queued command tags, and flushes pending native commands from the host command queue with system-specific error condition or re-issue pending queued commands.

**Transition HFPDMAQ11:1**: After the error flush actions have been completed, it shall transition to the HFPPIO: Idle state.
**HFPDMAQ12: RetrieveRequest_SenseDMA**: This state is entered when the device has the data ready to transfer a data FIS to the host containing the Queued Error Log contents.

When in this state, the device shall request that the Transport layer transmit a data FIS containing the data. The device command layer shall request a Data FIS size of no more than 2,048 Dwords.

**Transition HFPDMAQ13:1**: When the FIS has been transmitted, the device shall transition to the HFPDMAQ13: SendStatus state.

**HFPDMAQ13: SendStatus**: This state is entered when the device has transferred all of the data requested by the command or has encountered an error that causes the command to abort before completing the transfer of the requested data.

When in this state, the device shall request that the Transport layer transmit a Register Device to Host FIS with register content as described in the command description in the ATA8-ACS standard and the Interrupt bit set to one.

**Transition DDMA12HFPDMAQ13:1**: When the FIS has been transmitted, the device shall transition to the HFPDMAQ11: ErrorFlush state.