

**Proposed  
Draft**

**Serial ATA  
International Organization**

**Version 1  
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**Serial ATA Revision 3.2 ECN085  
Title : mSATA Figure Cleanup**

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## **1 Introduction**

### **1.1 Problem Statement**

The order of Figure 99 and Figure 100 were reversed. The text that should have pointed to Figure 99 had a typo and pointed to Figure 98.

### **1.2 Solution Summary**

Move Figure 99 below Figure 100 and call it Figure 100 (part 2 of 2). Fix figure reference from text to Figure 99. Cleaned up section titles based on CabCon conference call. Added (informative) to the figures based on feedback from the CabCon conference call.

### **1.3 Background (optional)**

## 2 Technical Specification Changes

### 2.1 <Title of section being changed>

[editor note: Existing text is black. New text is marked as underlined in blue color. Material to be deleted ~~is red with strikethrough markings.~~ ]

### 2.2 <6.6> mSATA connector

#### 2.2.1 <6.6.1> mSATA connector overview

This section defines the requirements of an mSATA configuration with a Serial-ATA interface. The definition supports the following capabilities:

- a) supports Gen1 (1.5 Gbps) and Gen2 (3 Gbps) transfer rates;
- b) support for mSATA;
- c) support of 3.3 V;
- d) support 4 vendor pins; and
- e) support 2 vendor pins, for drive or SSD manufacturing usage.

#### 2.2.2 <6.6.2> General description

The mSATA connector is designed to enable connection of a new family of small form factor devices to the Serial ATA interface.

The signal assignments are outlined in the tables below. All mSATA physical dimensions are under the control of JEDEC and provided in SATA as informative. See JEDEC MO-300 for all physical requirements.

#### 2.2.3 <6.6.3> mSATA host connector, footprint, and mounting—~~Connector location on mSATA host~~

See JEDEC MO-300 for form factor definition and connector location.

The mSATA host connector physical dimensions are shown as informative in Figure 96.~~location is defined in the drawing below.~~ The mSATA host footprint and keepout are shown as informative in Figure 97 and Figure 98.

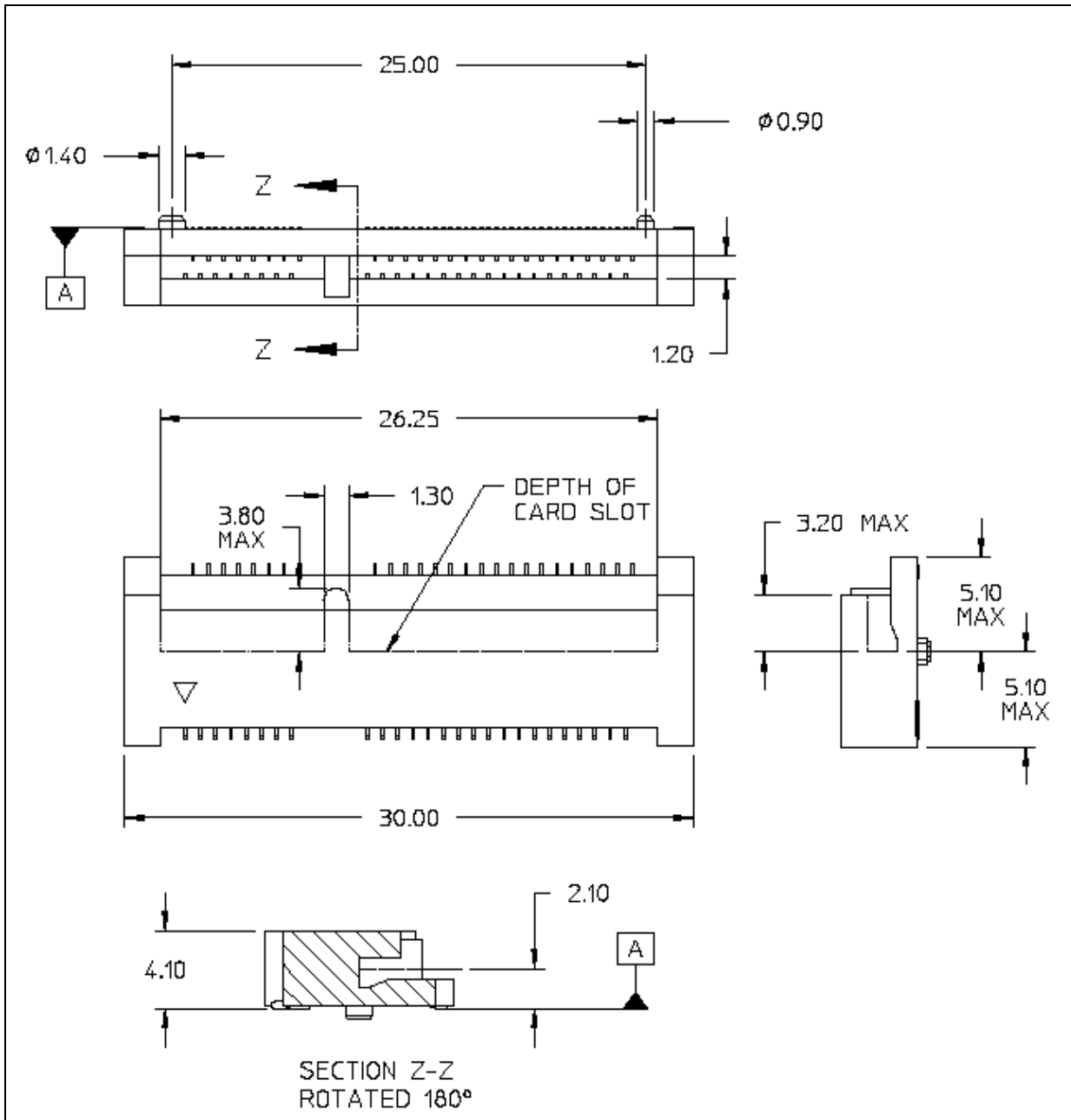


Figure 96 – mSATA card connector [\(informative\)](#)

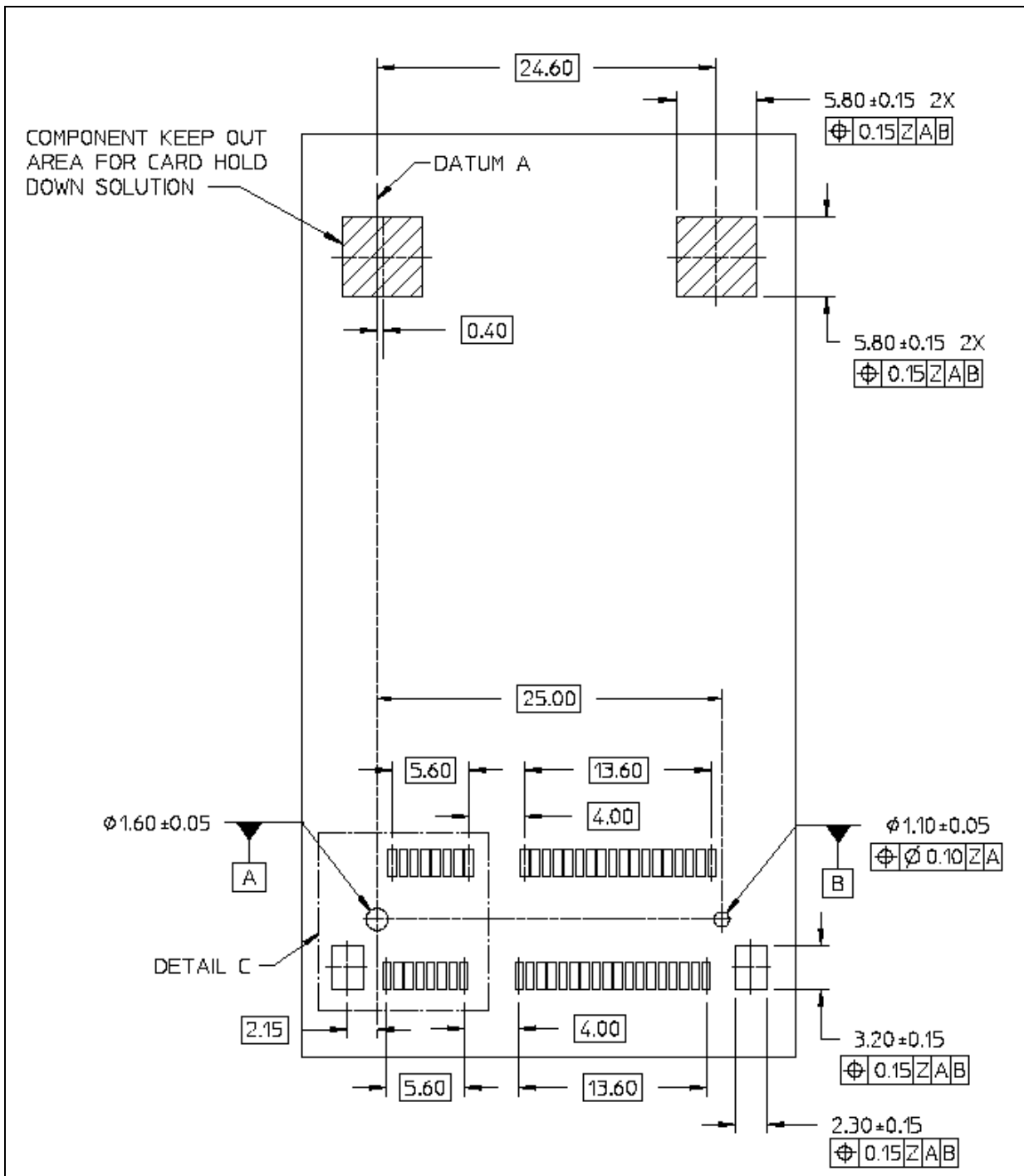


Figure 97 – mSATA card footprint and keepout [\(informative\)](#)

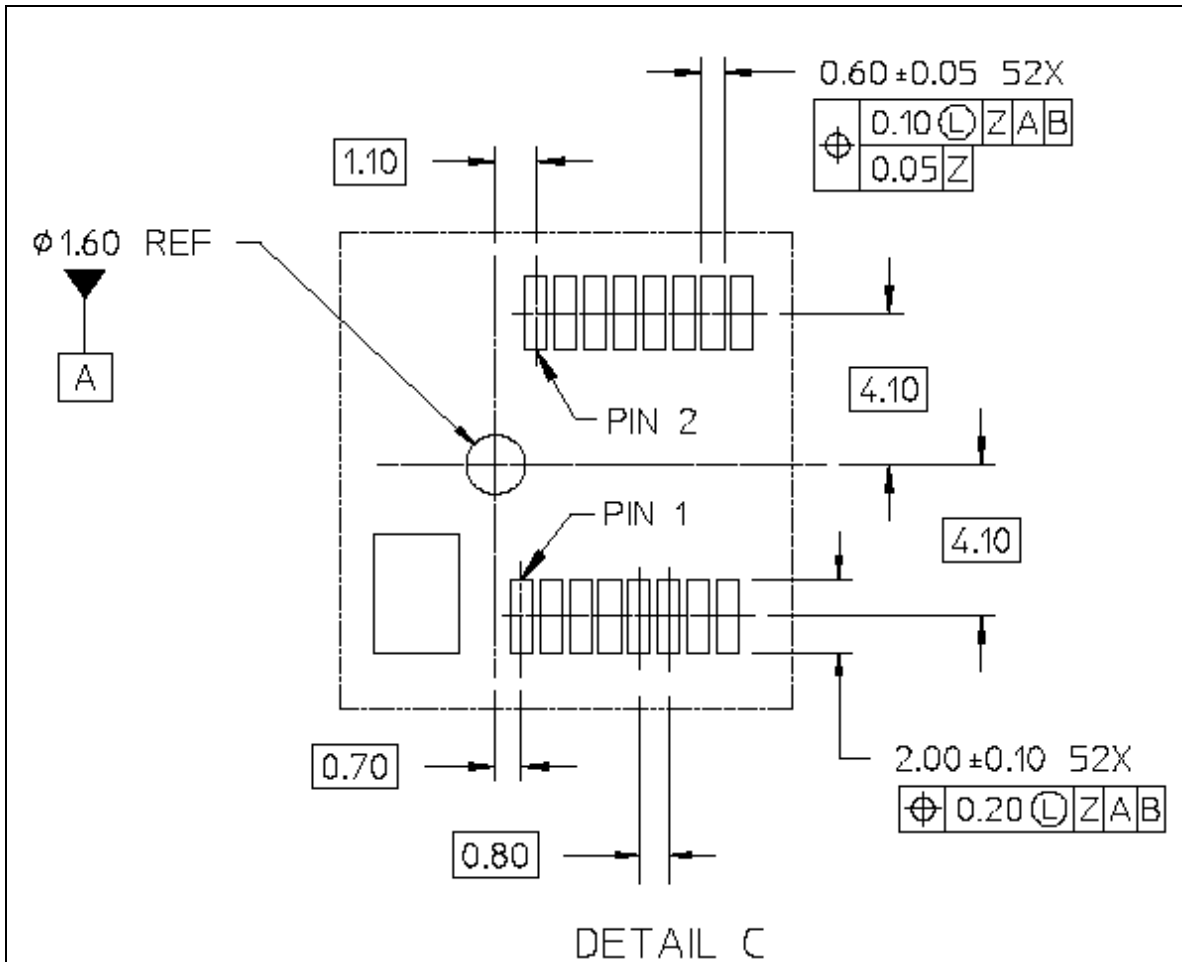


Figure 98 – mSATA card connector location detail C [\(informative\)](#)

## 2.2.4 <6.6.4> [mSATA device dimensions](#) **Mating-interfaces**

### 2.2.4.1 <6.6.4.1> **Device mSATA card embedded type connector**

Figure 100 defines the device mSATA card type internal connector.

### 2.2.4.2 <6.6.4.2> **mSATA interface dimensions**

~~Figure 98 defines the interface dimensions for the mSATA connector.~~

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Editor's note: The reference to Figure 98 was an error and should have pointed to Figure 99.

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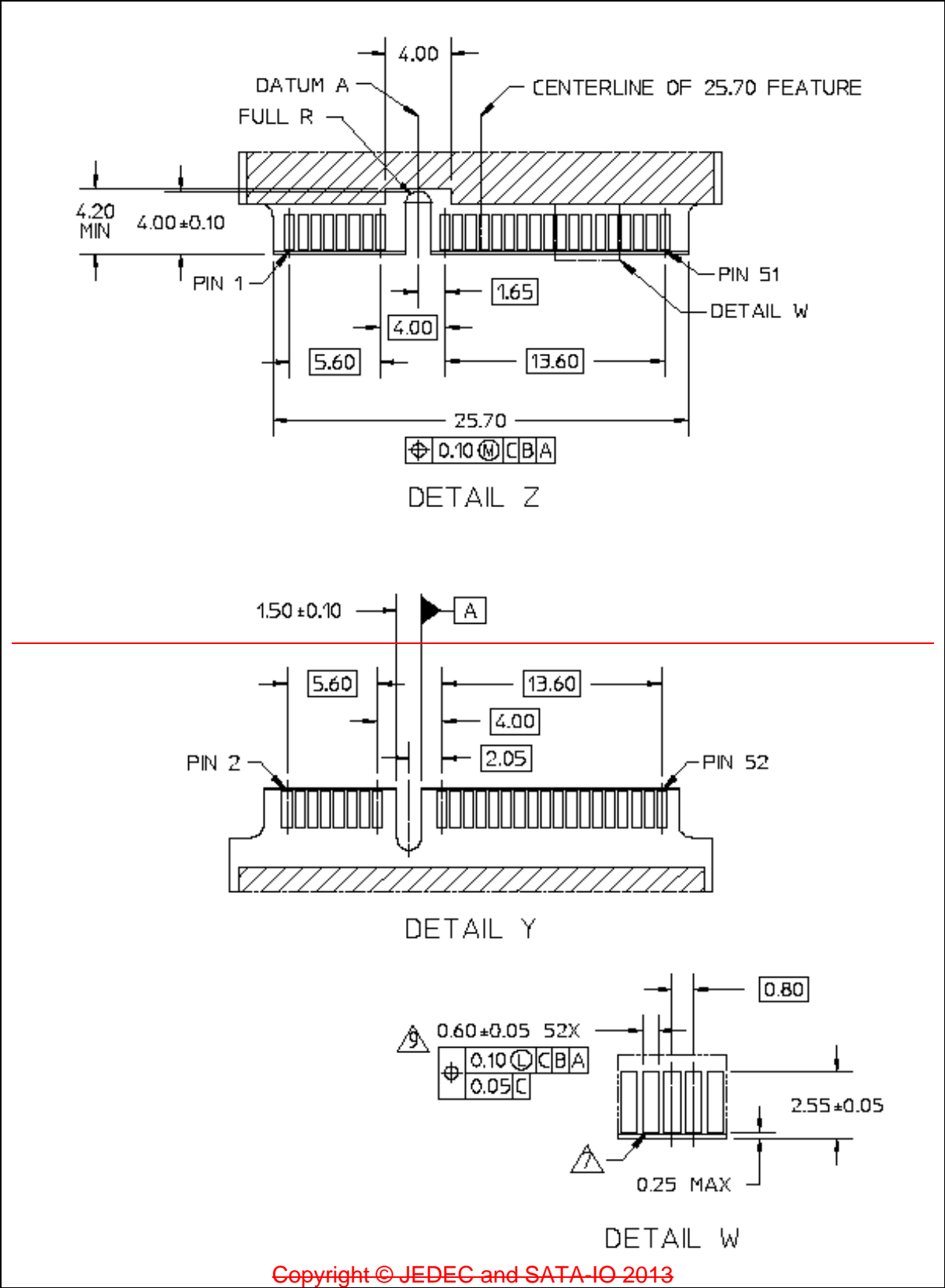


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Editor's note: Move Figure 99 below Figure 100 because Figure 99 contains detailed views called out in Figure 100. Figure 99 once moved below Figure 100 is called Figure 100 (part 2 of 2).

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Figure 99 – mSATA card connector location detail W, Y and Z

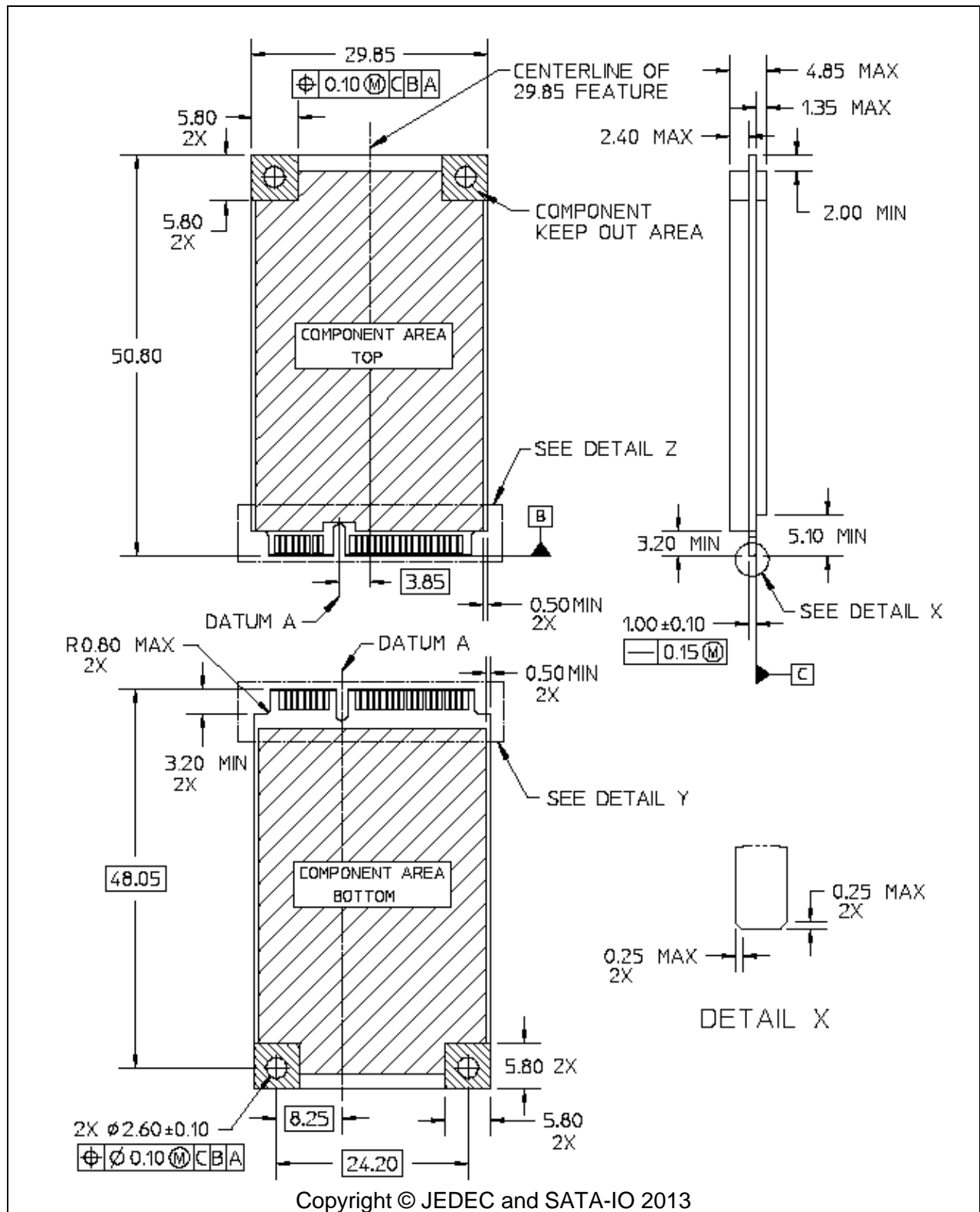


Figure 100 – Device mSATA card type internal connector [\(informative\)](#)

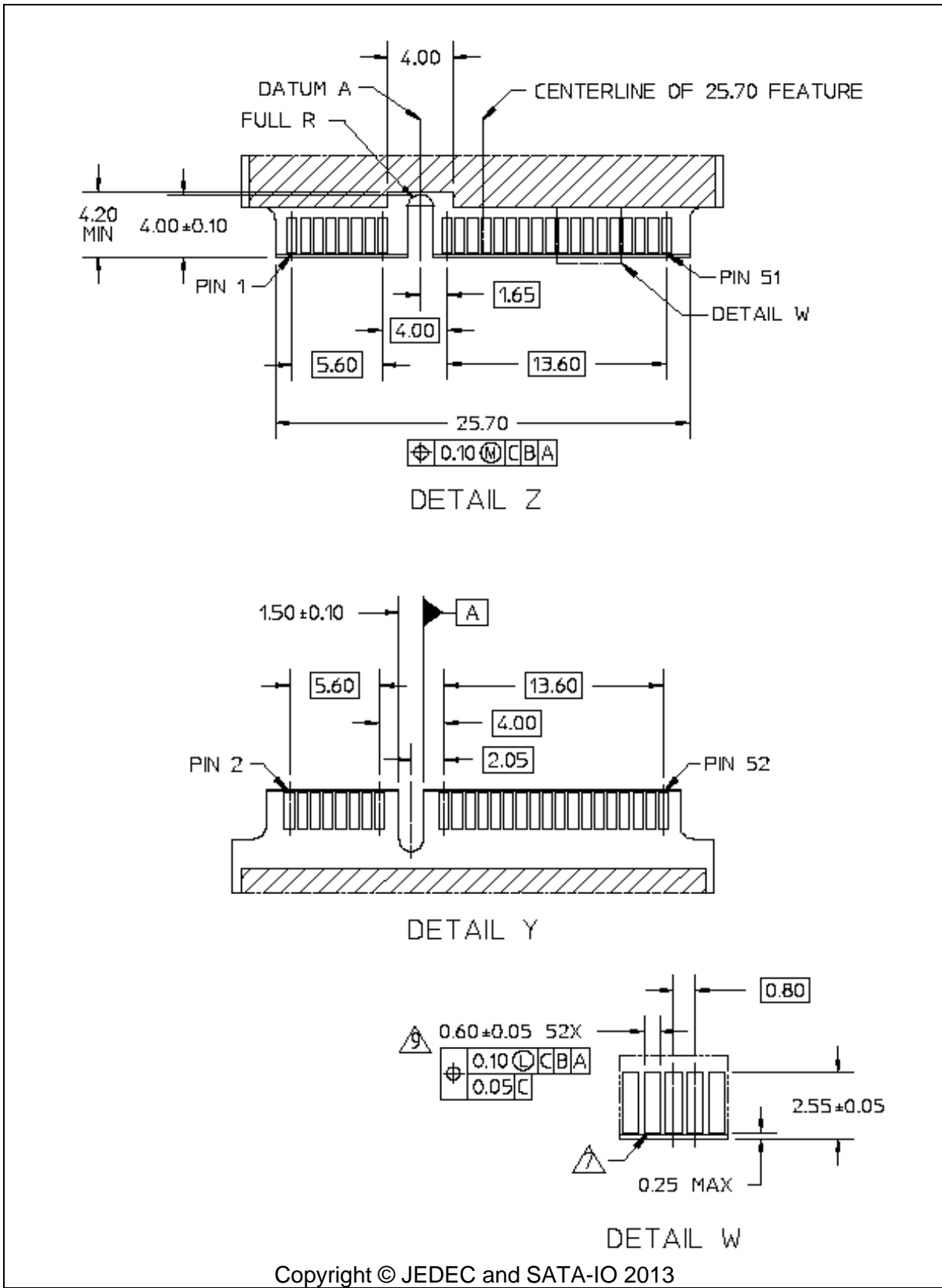


Figure 100 – Device mSATA card type internal connector (informative) (part 2 of 2)