

**Proposed
Draft**

**Serial ATA
International Organization**

**Version 2
09/26/2016**

**Serial ATA Revision 3.3 ECN 090
Title : Correction of reference to SRST**

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Document History

Version	Date	Comments
0	09/19/2016	Initial release.
1	09/19/2016	Renamed from TPR to ECN and removed 'volume 1' from the cross reference
2	09/26/2016	Renamed to ECN090
3	10/24/2016	Changed cross reference from ATA/ATAPI-7 to AST-2, on the recommendation of T13.

1 Introduction

The location of the SRST bit in the Device Control register was removed by INCITS/T13 after ATA/ATAPI-7 volume 1, so SATA Revision 3.3 must reference that document for the location of SRST.

[At the T13 plenary in Oct. 2016, T13 recommended that AST-2 would be a better and longer-lived cross reference, since ATA/ATAPI-7 is old already.](#)

2 Technical Specification Changes

[editor's note: add the following to 3.3 References under development]

[ATA/ATAPI Serial Transport \(AST-2\) \[INCITS BSR T13/535\]](#)

[editor's note: add the following cross reference]

11.4 Software reset protocol

If the host sends a Register Host to Device FIS with a one in the SRST bit position ([see ATA/ATAPI-7 AST-2](#)) of the Device Control register byte, regardless of the device power management mode (e.g., SLEEP, STANDBY), the device shall perform the software reset protocol (see Figure 314).