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## **SATA-IO Releases New DevSleep Feature for Always On Always Connected Functionality in Ultra-Portable Computing Devices**

*New standard significantly lowers power consumption and extends battery life for high-speed devices.*

**Beaverton, Ore. – January 05, 2012** – [The Serial ATA International Organization](http://www.sata-io.org) (SATA-IO), the industry consortium dedicated to sustaining the quality, integrity and dissemination of Serial ATA (SATA™) technology, today announced the completion of SATA DevSleep™, a new feature that reduces power consumption of devices. SATA DevSleep enables the development of ultra-portable computing devices based on SATA storage solutions that consume significantly less power than current offerings, resulting in longer battery life and enhanced ease of use.

DevSleep defines the lowest power state for SATA technology, allowing the PHY and other circuitry to be completely powered off, unlike existing partial/slumber states, which require a partially powered PHY. The feature allows devices to be always on and always connected without unnecessarily reducing battery life. For example, with DevSleep an ultra-thin laptop that would have previously needed to be put into standby to conserve battery life can now stay on and be immediately available for use.

“Consumers want thinner, faster mobile computing products that can go a long time without being tethered to an outlet,” said Mladen Luksic, SATA-IO president. “SATA DevSleep will enable manufacturers to take advantage of the cost savings and performance benefits of SATA, while addressing the needs of these low power mobile applications and enabling innovative designs for countless future mobile devices.”

Power consumption has become an increasingly critical consideration when selecting data storage components, as consumers are more mobile and less inclined to be “plugged in.” SATA-IO has developed a number of power management protocols to minimize power

consumption and extend battery life in SATA devices. DevSleep expands these options by enabling SATA technology to be implemented in ultra-portable devices without negatively impacting battery life or limiting device response time.

More information on all of the power management features available for SATA technology is available at [http://www.sata-io.org/technology/power\\_management.asp](http://www.sata-io.org/technology/power_management.asp).

### **The Growing SATA Ecosystem**

Since its introduction in 2001, SATA technology has penetrated 99% of the PC market and evolved to provide a variety of storage options in the battery-operated notebook computer market. Key specifications for implementing SATA technology in this device segment include:

- **[SATA  \$\mu\$ SSD™](#)**: An embedded, single-chip solution that connects directly to the motherboard to enable ultra-thin form factor devices.
- **[SATA Express](#)**: SATA Express is a new specification under development by SATA-IO that combines SATA software infrastructure with the PCI Express® (PCIe®) interface to deliver high-speed storage solutions.

More information on these and other SATA specifications is available at [www.sata-io.org](http://www.sata-io.org).

### **About SATA-IO**

Formed in September 2004, SATA-IO is the International Organization that owns and manages Serial ATA specifications as open industry standards. The organization defines and implements the Serial ATA specifications as the industry's storage needs evolve. It is dedicated to sustaining the quality, integrity and dissemination of SATA technology by maintaining the specifications, promoting and marketing the benefits of the technology and creating future interface features and specifications that carry storage into the next decade. Additional information about the organization, its participating companies and membership is available at [www.sata-io.org](http://www.sata-io.org).